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**1 Practical refactoring-based framework upgrade**

Ilie Savga, Michael Rudolf, Sebastian Götz, Uwe Almann

 October 2008 **GPCE '08: Proceedings of the 7th international conference on component engineering**
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Although the API of a software framework should stay stable, in practice maintenance. When deploying a new framework version such changes are used one of its previous versions. While manual plugin ...

**Keywords:** adaptation, frameworks, maintenance, refactoring

**2 Two diet plans for fat PDF**

Thomas A. Phelps, Robert Wilensky

 November 2003 **DocEng '03: Proceedings of the 2003 ACM symposium on**
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**Keywords:** PDF, compact PDF, compression, multivalent

**3 Incentive-compatible interdomain routing**

Joan Feigenbaum, Vijay Ramachandran, Michael Schapira

 June 2006 **EC '06: Proceedings of the 7th ACM conference on Electronic c**
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The routing of traffic between Internet domains, or *Autonomous System interdomain routing*, is currently handled by the Border Gateway Protocol. autonomous systems can apply semantically rich ...


**Keywords:** border gateway protocol (BGP), distributed algorithmic me

#### 4 The missing link: dynamic components for ML

 Andreas Rossberg

September 2006 **ICFP '06: Proceedings of the eleventh ACM SIGPLAN inter programming**

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Despite its powerful module system, ML has not yet evolved for the mo modular programming, to which more primitive languages have adapted design and semantics of a simple yet expressive firstclass ...

**Keywords:** components, distributed programming, dynamic linking, dy separate compilation, units

Also published in:

September 2006 **SIGPLAN Notices** Volume 41 Issue 9

#### 5 An efficient register optimization algorithm for high-level synthesis fr specifications

 Ranga Vernuri, Srinivas Katkoori, Meenakshi Kaul, Jay Roy

January 2002 **Transactions on Design Automation of Electronic System**

**Publisher:** ACM  [Request Permissions](#)

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We address the problem of register optimization that arises during high hierarchical behavioral specifications. Register optimization is the proce group can be safely allocated to a hardware ...

**Keywords:** Behavioral synthesis, hardware description languages, hier synthesis, lifecycle analysis, register optimization

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